

## SCPR Board Mods and Notes

### CF2 Main PC Board

1. U5 (CF2) break trace between C47 & C49
2. U6-15 & H1-14 break track
3. U6-12 & H1-14 add jumper wire

The CF2 PCSx decode conflicts with the HC138 SPI select design so SPI7 is not available. This moves it to SPI3. (There are 15 CF2 SPI slots available. For future designs, control HC138 G1 (pin 6) with a TPU pin. PCSx can then decode 0-14. 15 is the null state.)

### Interface PC Board

1. U6-4 needs a 100K pulldown resistor to DCOM.  
(shouldn't but something with the VHC1G04 vs HC597?)